



# Simtek Corporation Product Change Notification [PCN]

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PCN # **10083.0** Type **FINAL** PCN Date **February 14, 2008**

Content Category **Notes on Product Application** Impact **Datasheet Specification**

Product Category **NVSRAM** Change Type [JESD46] **MINOR**

PCN Title **Preliminary Datasheet Update: STK14EC16 256Kx16 AutoStore nvSRAM, Simtek Doc # ML0061 Rev 1.1**

PCN Detail [page 2: Products page 3: Datasheet changes, Additional Information] **See page 3 for full detail of changes.**

Background Reason for Change Issue Addressed **Datasheet review and addition of "best practice" in use of the product.**

Summary of Product[s] Affected [full listing on Page 2] **STK14EC16 256Kx16 AutoStore nvSRAM**

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This datasheet is now available on Simtek website at [www.simtek.com](http://www.simtek.com)**

Response to this PCN **As above.**

Respond By Date: **March 15, 2008** Samples Available From **n/a**

Target Product Availability Date **n/a** Transition Date/Datecode **n/a**



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## SIMTEK PRODUCT AFFECTED BY THIS PCN

STK14EC16-TF15  
STK14EC16-TF15TR  
STK14EC16-TF25  
STK14EC16-TF25TR  
STK14EC16-TF45  
STK14EC16-TF45TR  
STK14EC16-UF15  
STK14EC16-UF15TR  
STK14EC16-UF25  
STK14EC16-UF25TR  
STK14EC16-UF45  
STK14EC16-UF45TR  
STK14EC16-BF15  
STK14EC16-BF15TR  
STK14EC16-BF25  
STK14EC16-BF25TR  
STK14EC16-BF45  
STK14EC16-BF45TR  
STK14EC16-TF15I  
STK14EC16-TF15ITR  
STK14EC16-TF25I  
STK14EC16-TF25ITR  
STK14EC16-TF45I  
STK14EC16-TF45ITR  
STK14EC16-UF15I  
STK14EC16-UF15ITR  
STK14EC16-UF25I  
STK14EC16-UF25ITR  
STK14EC16-UF45I  
STK14EC16-UF45ITR  
STK14EC16-BF15I  
STK14EC16-BF15ITR  
STK14EC16-BF25I  
STK14EC16-BF25ITR  
STK14EC16-BF45I  
STK14EC16-BF45ITR



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## CHANGES TO DATASHEET

See page 1 and change summary on last page of published data sheet.

Page 1: revised block diagram

Page 2: added new 48 FBGA information and added pin descriptions for pins E and W.

Page 3: added thermal characteristics. In the DC Characteristics table, revised values for Icc2, Icc4, ISB, VIH, Vcc, and VCAP; and changed Industrial Max Value of VCAP to 180 and revised VCAP notes. Added "(except HSB)" to notes for Output Logic "1" Voltage.

Page 5: in SRAM Read Cycles #1 & #2 table, revised parameter description for tELQX and tEHQZ and changed Symbol #2 to tELEH for Read Cycle Time; updated SRAM Read Cycle #2 timing diagram and changed title to add G controlled.

Page 7: in AutoStore/Power-Up Recall table, changed max value for #23 (t) to 12.5.STORE.

Page 8: in Software-Controlled Store/Recall Cycle table, revised values for tRECALL; revised the notes below the Software-Controlled Store/Recall Cycle diagram.

Page 10: in Mode Selection table, changed column to A18-A0; in the values in this column, added a zero after each instance of "0x"; changed AutoStore Enable value to 0x04B46.

Page 11: in Auto-Store Operation, deleted line about VCAP pin being driven to 5V by a charge pump internal to the chip.

Revised text: "Refer to the DC CHARACTERISTICS table for the size of the capacitor."

Page 12: under Hardware Store (HSB) Operation, revised first paragraph to read "The HSB pin has a very resistive pullup..."

Page 13: added best practices section.

Page 15: in Ordering Information, Lead Finish, replaced "Sn (Matte Tin) RoHS Compliant" with "Nickel/Palladium/Gold (Ni/Pd/Au)"; added "B = Plastic 48-pin FBGA (Fine Pitch Ball Grid Array)" to Finish.

Page 16: in Ordering Codes, added ordering information for 48 FBGA and added access times column.

## Additional PCN Information [continued from Page 1]

None.