



Simtek Corporation Product Change Notification [PCN]

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PCN # **10080.0** Type **FINAL** PCN Date **February 14, 2008**

Content Category **Notes on Product Application** Impact **None. This PCN for Information**

Product Category **NVSRAM W/RTC** Change Type [JESD46] **MINOR**

PCN Title **Datasheet Update: STK17TA8 1M 3v nvSRAM w/RTC, Simtek Doc # ML0025 Rev 2.0**

PCN Detail [page 2: Products page 3: Datasheet changes, Additional Information] **See page 3 for full detail of changes.**

Background Reason for Change Issue Addressed **Datasheet review and addition of "best practice" in use of the product.**

Summary of Product[s] Affected [full listing on Page 2] **STK17TA8 1M 3v nvSRAM w/RTC**

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This datasheet is now available on Simtek website at www.simtek.com**

Response to this PCN **As above.**

Respond By Date: **March 15, 2008** Samples Available From **n/a**

Target Product Availability Date **n/a** Transition Date/Datecode **n/a**



Product Change Notification [PCN] Simtek Corporation

PCN #: 10080.0

PCN Date: February 14, 2008

SIMTEK PRODUCT AFFECTED BY THIS PCN

STK17TA8-RF25
STK17TA8-RF25I
STK17TA8-RF45
STK17TA8-RF45I



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CHANGES TO DATASHEET

See page 1 and change summary on last page of published data sheet.

Page 3: added thermal characteristics. For DC characteristics,
Page 5: revised recommended value verbiage. Page 6: in the SRAM Read Cycles #1 and #2 table, revised parameter description for tELQX and tEHQZ and changed Symbol #2 to tELEH for Read Cycle Time; updated SRAM Read Cycle #2 timing diagram and changed title to add G controlled.
Page 9: revised the notes below the Software-Controlled Store/Recall Cycle diagram.
Page 11: in the Mode Selection table, changed column to A16-A0 and added a 0 after all instances of "0x".
Page 12: under AutoStore Operation, revised text to read: "Refer to the DC CHARACTERISTICS table for the size of the capacitor."
Page 13: under Hardware Store (HSB) Operation, revised first para-graph to read "The HSB pin has a very resistive pullup..."
Page 14: added best practices section. Revised RTC register map for registers 0x1FFF8 (D7) and 0x1FFF6 (D7, D6, D5, D3, and D2).
Page 25: added access times column to the Ordering Codes.

Additional PCN Information [continued from Page 1]

None.